10079115 - 081502

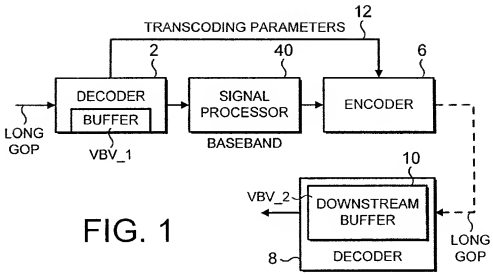

FIG. 1

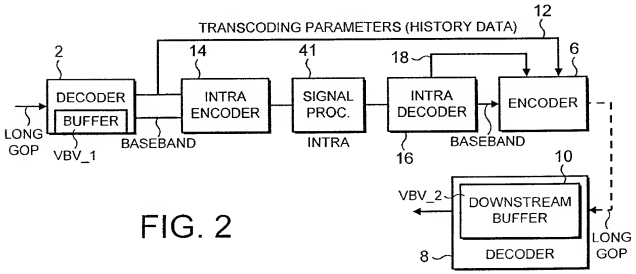

FIG. 2

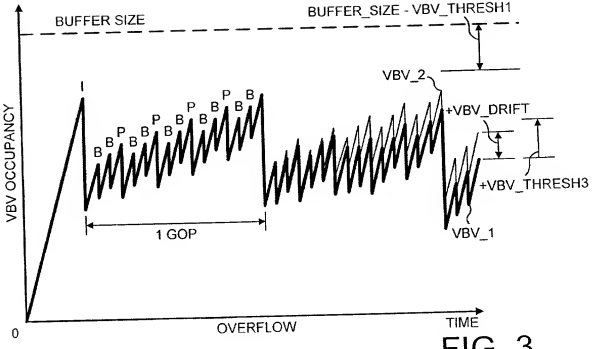
BUFFER SIZE
BUFFER_SIZE - VBV_THRESH1
VBV_2
+VBV_DRIFT
+VBV_THRESH3
VBV_1
VBV OCCUPANCY
I
B B P B B P B B P B B P B B
1 GOP
0
OVERFLOW
TIME

FIG. 3

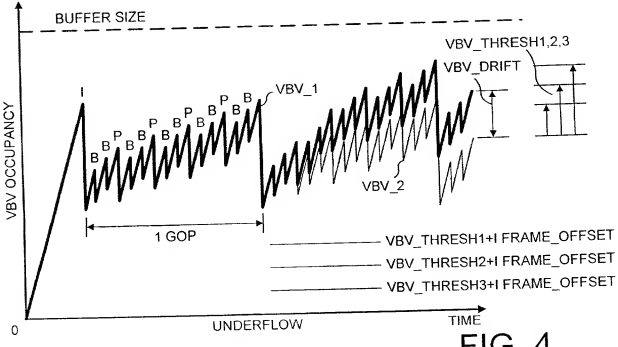
BUFFER SIZE
VBV_THRESH1,2,3
VBV_DRIFT
VBV_1
VBV_2
VBV OCCUPANCY
I
B B P B B P B B P B B P B B
1 GOP
VBV_THRESH1+I FRAME_OFFSET
VBV_THRESH2+I FRAME_OFFSET
VBV_THRESH3+I FRAME_OFFSET
0
UNDERFLOW
TIME

FIG. 4

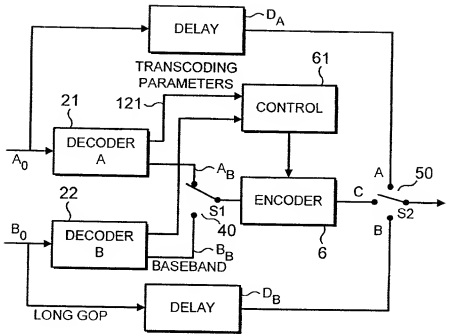
DELAY
D_A
TRANSCODING PARAMETERS
61
CONTROL
21
121
DECODER A
A_0
A_B
A
50
ENCODER
C
S2
22
S1
40
B
DECODER B
B_0
B_B
BASEBAND
6
LONG GOP
DELAY
D_B

FIG. 5

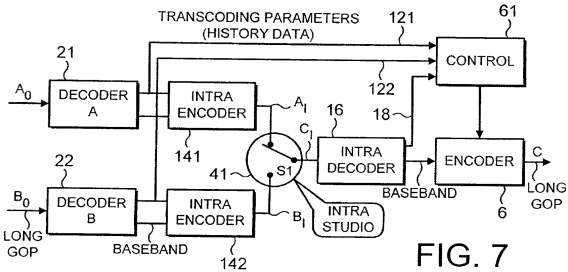
TRANSCODING PARAMETERS (HISTORY DATA)
121
61
CONTROL
21
122
DECODER A
A_0
INTRA ENCODER
A_I
16
18
C_I
141
INTRA DECODER
ENCODER
C
22
41
S1
LONG GOP
DECODER B
B_0
INTRA ENCODER
B_I
INTRA STUDIO
BASEBAND
6
LONG GOP
BASEBAND
142

FIG. 7

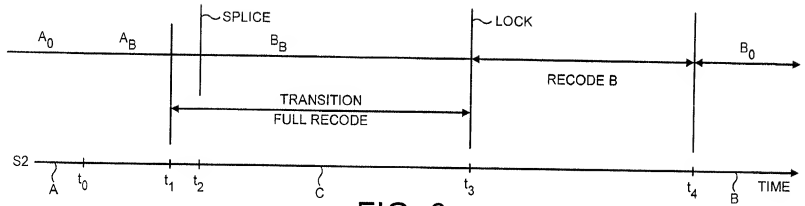
SPLICE
LOCK
A_0
A_B
B_B
B_0
RECODE B
TRANSITION
FULL RECODE
S2
A
t_0
t_1
t_2
C
t_3
t_4
B
TIME

FIG. 6

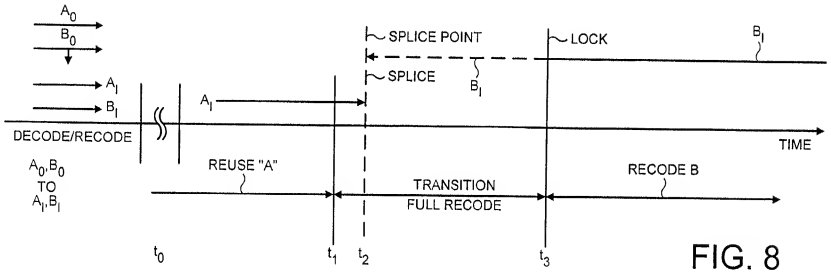
A_0
B_0
A_I
B_I
DECODE/RECODE
A_0, B_0
TO
A_I, B_I
SPLICE POINT
SPLICE
LOCK
B_I
A_I
TIME
REUSE "A"
TRANSITION
FULL RECODE
RECODE B
t_0
t_1
t_2
t_3

FIG. 8